

In the Specification**Amend the specification as follows:****Amend the paragraph beginning at page 1, line 3 as follows:**

A2
This application is related to subject matter described and claimed in U.S. patent application serial no. ~~(att. docket no. FIS990318US)~~ 09/597,525 entitled "High Performance Nonblocking Parallel Storage Manager For Parallel Software," now U.S. Patent No. 6,507,903 and U.S. patent application serial no. ~~(att. docket no. FIS990319US)~~ 09/597,524 entitled "Method Of Using A Distinct Flow Of Computational Control As A Reusable Data Object", both by the inventors of the instant application and filed on even date herewith.

Amend the paragraph beginning at page 1, line 14 as follows:

A3
In prior art computing using separate, non-parallel processing, the programs often share data and other services. An example of this is shown in Fig. 1 where separate process memories 19a, 19b, which may be physically separated in different memory storage, or logically separated in the same memory storage, contain global variable memory 20a, ~~20b~~ for data items visible to the entire process, heap memory 21a, ~~21b~~ for data structure, stack memory 23a, ~~23b~~ for function arguments, and local data items, and free memory space 22a, ~~22b~~ which may be utilized as needed for either heap or stack memory space. A portion of the free memory space may be designated as common memory 22c available to both program-A a, 24a, or program-B b, 24b, which operate in

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AB the separate process memories 19a, 19b, respectively. Each program A-a and B-b can access in the process memory only is what is designated in the common area 22c, but cannot access other memory between the programs. A programmer utilizing the system of Fig. 1 has relatively little assistance from the system in restricting access to data structures in common memory.
